



Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 3

Complete if Known

Application Number	10/815,604
Filing Date	03/31/2004
First Inventor	HO, Andrew
Art Unit	2611
Examiner	Perilla, Jason
Atty Docket #	
Rambus #	RA290.CIP1.US

US Patent Documents

Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
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	0000328 3	5732089	03/24/1998	Negi, Keiji	371	5.1
	0000375 4	2002-073373	06/13/2002	Nakao, Michinobu	714	738
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Examiner Signature:

/Jason Perilla/

Date Considered:

09/08/2009

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Sheet 2 of 3	Complete if Known	
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US Patent Documents						
Examiner Initials*	Cite No.	Document No.	Date (MM-DD-YYYY)	Patentee	Class	Subclass
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	0006307	WO 42	00/27065	05/11/2000	Broadcom Corp	H04L	1/24	
	0006408	EP 43	1 127 423 A0	08/29/2001	Broadcom Corporation	H04L	1/24	

Non-Patent Literature Documents				T**
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
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	0003620 45	MAXFIELD, C., "The Ouroboros of the Digital Consciousness: Linear-Feedback-Shift Registers," EDN, pp. 135-142, January 4, 1996		
	0003663 46	DALLY ET AL., "Multi-gigabit Signaling with CMOS," DARPA funded		

Examiner Signature:	/Jason Perilla/	Date Considered:	09/08/2009
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	46 Cont.	presentation, 22 pages, May 12, 1997.	
	0003901 47	COVA ET AL., "Characterization of Individual Weights in Transversal Filters and Application to CCD's, IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, December 1982, pp. 1054-1061.	
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	0004714 53	HOROWITZ, MARK, "Lecture 15: Transmitter and Receiver Design." Computer Systems Laboratory - Stanford University. Copyright 2000. Pages 15-1 to 15-35.	
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